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INFORMATION DISCLOSURE STATEMENT BY APPLICANT				FILING DATE: February 25, 2004	CONFIRMATION NO.: 2714
				APPLICANT: Richard P. Schubert	
Sheet	1	of	1	GROUP ART UNIT: 2186	EXAMINER: Not Yet Assigned

#### U.S. PATENT DOCUMENTS

Examiner's Initials	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication or of issue of Cited Document MM-DD-YYYY
		Number	Kind Code		

#### FOREIGN PATENT DOCUMENTS

Examiner's Initials	Cite No.	Foreign Patent Document			Name of Patentee or Applicant of Cited Document (not necessary)	Date of Publication of Cited Document MM-DD-YYYY	Translation (Y/N)
		Office/Country	Number	Kind Code			

#### OTHER ART — NON PATENT LITERATURE DOCUMENTS

Examiner's Initials	Cite No	Include name of the author (in CAPITAL LETTERS) title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, relevant page(s), volume-issue number(s), publisher, city and/or country where published.	Translation (Y/N)
CW	A1	Analog Devices, Inc., Blackfin Embedded Processor, ADSP-BF535; 2003, pp. 1-44	
CW	A2	Presentation Slides of Background Information on Memory System;	
CW	A3	Jim Handy, "The Cache Memory Book", Second Edition, Chapters 1 and 2; January 15, 1998.	

EXAMINER	DATE CONSIDERED
/Craig Walter/	01/10/2007

#EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

\*a copy of this reference is not provided as it was previously cited by or submitted to the office in a prior application, Serial No. \_\_\_, filed \_\_\_, and relied upon for an earlier filing date under 35 U.S.C. 120 (continuation, continuation-in-part, and divisional applications).

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